

66/80/60

Express Mail Label No. EL208754705US



09/10/99

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
49304(840)Total Pages in this Submission
66

TO THE ASSISTANT COMMISSIONER FOR PATENTS

Box Patent Application
Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

PIXEL DEFECT DETECTOR FOR SOLID-STATE IMAGING DEVICE

and invented by:

XIAOMANG ZHANG, NOBORU KUBO



If a CONTINUATION APPLICATION, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Which is a:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Which is a:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Enclosed are:

Application Elements

1. ☐ Filing fee as calculated and transmitted as described below
2. ☒ Specification having 61 Pages w/Cover Sheet pages and including the following:
 - a. ☒ Descriptive Title of the Invention
 - b. ☐ Cross References to Related Applications (if applicable)
 - c. ☐ Statement Regarding Federally-sponsored Research/Development (if applicable)
 - d. ☐ Reference to Microfiche Appendix (if applicable)
 - e. ☒ Background of the Invention
 - f. ☒ Brief Summary of the Invention
 - g. ☒ Brief Description of the Drawings (if drawings filed)
 - h. ☒ Detailed Description
 - i. ☒ Claim(s) as Classified Below
 - j. ☒ Abstract of the Disclosure

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
49304(840)

Total Pages in this Submission
66

Application Elements (Continued)

3. ☒ Drawing(s) (when necessary as prescribed by 35 USC 113)
- a. ☒ Formal Number of Sheets 5
- b. ☐ Informal Number of Sheets _____
4. ☐ Oath or Declaration
- a. ☐ Newly executed (original or copy) ☐ Unexecuted
- b. ☐ Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional application only)
- c. ☐ With Power of Attorney ☐ Without Power of Attorney
- d. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application,
see 37 C.F.R. 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference (usable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. ☐ Computer Program in Microfiche (Appendix)
7. ☐ Nucleotide and/or Amino Acid Sequence Submission (if applicable, all must be included)
- a. ☐ Paper Copy
- b. ☐ Computer Readable Copy (identical to computer copy)
- c. ☐ Statement Verifying Identical Paper and Computer Readable Copy

Accompanying Application Parts

8. ☐ Assignment Papers (cover sheet & document(s))
9. ☐ 37 CFR 3.73(B) Statement (when there is an assignee)
10. ☐ English Translation Document (if applicable)
11. ☐ Information Disclosure Statement/PTO-1449 ☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Acknowledgment postcard
14. ☒ Certificate of Mailing
- ☐ First Class ☒ Express Mail (Specify Label No.): EL208754705US

66/80/60



UTILITY PATENT APPLICATION TRANSMITTAL

(Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
49304(840)

Total Pages in this Submission
66

Accompanying Application Parts (Continued)

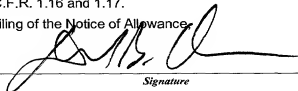
15. ☒ Certified Copy of Priority Document(s) (if foreign priority is claimed)
 Certified Copy of Japanese Patent Application No. 10-254428, Filed 9/8/98
16. ☐ Additional Enclosures (please identify below):

Fee Calculation and Transmittal

CLAIMS AS FILED

For	#Filed	#Allowed	#Extra	Rate	Fee
Total Claims	17	- 20 =	0	x \$18.00	\$0.00
Indep. Claims	2	- 3 =	0	x \$78.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
BASIC FEE					\$760.00
OTHER FEE (specify purpose)					
TOTAL FILING FEE					\$760.00

- ☐ A check in the amount of _____ to cover the filing fee is enclosed.
- ☐ The Commissioner is hereby authorized to charge and credit Deposit Account No. _____ as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of _____ as filing fee.
- ☐ Credit any overpayment.
- ☐ Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.
- ☐ Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance pursuant to 37 C.F.R. 1.311(b).


 Signature

Dated: September 8, 1999

David G. Conlin
 Reg. No. 27026
 Dike, Bronstein, Roberts & Cushman, LP
 130 Water Street
 Boston, MA 02109
 617-523-3400

cc:

66/80/60



A

CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)		Docket No. 49304(840)	
Applicant(s): Xiaomang Zhang, et al			
Serial No. Not Yet Assigned	Filing Date Filed Herewith	Examiner Not Yet Assigned	Group Art Unit Not Yet Assigned
Invention: PIXEL DEFECT DETECTOR FOR SOLID-STATE IMAGING DEVICE			
<p>I hereby certify that this <u>UTILITY PATENT APPLICATION</u> <i>(Identify type of correspondence)</i></p> <p>is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231 on <u>September 8, 1999</u> <i>(Date)</i></p> <p><u>Holly F. Malarney</u> <i>(Typed or Printed Name of Person Mailing Correspondence)</i></p> <p><u><i>Holly F. Malarney</i></u> <i>(Signature of Person Mailing Correspondence)</i></p> <p><u>EL208754705US</u> <i>("Express Mail" Mailing Label Number)</i></p>			
Note: Each paper must have its own certificate of mailing.			

99R00174

PIXEL DEFECT DETECTOR FOR SOLID-STATE IMAGING DEVICE

- 1 -

BACKGROUND OF THE INVENTION

1. FIELD OF THE INVENTION:

5 The present invention relates to a pixel defect detector which is provided in a solid-state imaging device such as a CCD for automatically detecting a defect produced in the solid-state imaging device.

2. DESCRIPTION OF THE RELATED ART:

10 It is generally known in the art that the quality of a solid-state imaging device such as a CCD may deteriorate due to a local crystal defect (also referred to as a "blemish") which is produced therein while being manufactured. Moreover, an additional blemish may be
15 produced in the solid-state imaging device after shipping by the irradiation of cosmic rays. Such blemishes can be classified into white blemishes and black blemishes.

Figure 6 shows a graph illustrating output
20 characteristics in response to the amount of incident light of different photoelectric transducers (referred to also as "pixels") provided in a solid-state imaging device. In Figure 6, the solid line A represents an output characteristic of a normal pixel, the broken line B

- 2 -

represents that of a pixel having a white blemish, and the one-dot chain line C represents that of a pixel having a black blemish. As can be seen from the output characteristic represented by the broken line B, a white
5 blemish is a defect due to which a bias voltage is always added to an output of the pixel. Moreover, as can be seen from the output characteristic represented by the one-dot chain line C, a black blemish is a defect by which the sensitivity of the pixel is reduced.

10

Conventionally, various devices and methods have been proposed in the art for detecting such a blemish in a pixel and correcting the output of such a pixel.

15

For example, Japanese Laid-Open Publication No. 8-195909 discloses an arrangement comprising: a ROM for storing data representing the position and the level of a CCD blemish which is already present prior to shipping; and an EEPROM for storing data representing the
20 position and the level of a CCD blemish which is additionally produced after the shipping. According to this arrangement, the output of the CCD having a blemish is corrected based on the data stored in the ROM and the data stored in the EEPROM.

Japanese Laid-Open Publication No. 7-7675 discloses a technique in which a blemish of a CCD is detected after performing an inverse γ correction on an output
5 signal of the CCD. The inverse γ correction is performed because once a γ correction is performed, the amount of error occurring in the output signal of the CCD due to the blemish is reduced, whereby the blemish detection is difficult.

10

Japanese Laid-Open Publication No. 62-8666 discloses a technique of substituting the output signal of a pixel having a blemish with the output signal of another pixel in the same row or column.

15

In the prior art, however, the detection of a blemish in a solid-state imaging device either before or after shipping requires a reference light generator for producing various (dark to bright) levels of reference
20 light incident upon the device, an aided system, and the like. Moreover, in the prior art, the detection of a blemish in a pixel and the correction of the output from the pixel may be difficult for anyone but an expert operator. These operations are far from those which can

- 4 -

ordinarily be done by a user.

SUMMARY OF THE INVENTION

5 According to one aspect of this invention, a pixel defect detector for a solid-state imaging device comprising a plurality of photoelectric transducers is provided. The pixel defect detector includes: a calculation section for obtaining output characteristics
10 of a subject photoelectric transducer for varied amounts of light incident thereupon so as to determine the presence/absence of a defect in the subject photoelectric transducer based on the output characteristics thereof.

15 In one embodiment of the invention, the pixel defect detector further includes a picture memory for storing an output signal from the photoelectric transducer. The calculation section determines the output characteristics of the subject photoelectric transducer
20 using the output signal of the subject photoelectric transducer stored in the picture memory.

 In one embodiment of the invention, the output characteristics of the subject photoelectric transducer

- 5 -

are represented by a plurality of output signals of the subject photoelectric transducer in response to different amounts of light incident thereupon, respectively.

5 According to another aspect of this invention, a pixel defect detector for a solid-state imaging device comprising a plurality of photoelectric transducers is provided. The pixel defect detector includes: a picture memory for storing outputs from a subject photoelectric
10 transducer in response to different amounts of light incident thereupon, respectively; and a calculation section for determining a photoelectric coefficient *a* of the subject photoelectric transducer and an offset output level *b* of the subject photoelectric transducer in the
15 absence of incident light based on the amounts of incident light, the outputs from the subject photoelectric transducer therefor, and Expression (1) below, so as to compare the photoelectric coefficient *a* and the offset output level *b* with a predetermined reference
20 photoelectric coefficient *a*₀ and a predetermined reference offset output level *b*₀, respectively, thereby determining the presence/absence of a defect in the subject photoelectric transducer:

$$y(x)=ax+b \qquad \dots (1)$$

- 6 -

where $y(x)$ denotes the output of the subject photoelectric transducer, and x denotes the amount of incident light.

5 In one embodiment of the invention, the pixel defect detector further includes an optical system for projecting a picture onto the solid-state imaging device. The output of the photoelectric transducer is determined while the optical system is defocused with respect to the
10 solid-state imaging device.

 In one embodiment of the invention, the amounts of light incident upon the subject photoelectric transducer comprise an amount of incident light when no
15 light is incident upon the solid-state imaging device and another amount of incident light which brings the solid-state imaging device to a near-overflow state.

 In one embodiment of the invention, the amount of
20 incident light x is determined by applying, to Expression (2) below, the predetermined reference photoelectric coefficient a_0 , the predetermined reference offset output level b_0 , and a reference output signal y_0 :

$$x = (y_0 - b_0) / a_0 \quad \dots (2).$$

- 7 -

In one embodiment of the invention, the output y_0 is set to a median among outputs from a plurality of photoelectric transducers neighboring the subject photoelectric transducer.

In one embodiment of the invention, the neighboring photoelectric transducers comprise only those which display the same one of a plurality of colors to be displayed as that of the subject photoelectric transducer.

In one embodiment of the invention, the presence/absence of a defect in the subject photoelectric transducer is determined by applying the photoelectric coefficient a of the subject photoelectric transducer, the offset output level b of the subject photoelectric transducer, the reference photoelectric coefficient a_0 , and the reference offset output level b_0 , to Expression (3) below:

no defect, if $|a_0 - a| < \Delta a$ and $|b_0 - b| < \Delta b \dots (3)$

where Δa and Δb are predetermined threshold values.

- 8 -

In one embodiment of the invention, the presence/absence and the type of defect in the subject photoelectric transducer are determined by applying the photoelectric coefficient a of the subject photoelectric transducer, the offset output level b of the subject photoelectric transducer, the reference photoelectric coefficient a_0 , and the reference offset output level b_0 , to Expression (4) below:

no defect, if $|a_0 - a| < \Delta a$ and $|b_0 - b| < \Delta b$;
 a black blemish, if $|a_0 - a| \geq \Delta a$; and
 a white blemish, if $|b_0 - b| \geq \Delta b$...
 (4)
 where Δa and Δb are predetermined threshold values.

15

In one embodiment of the invention, the reference photoelectric coefficient a_0 and the reference offset output level b_0 are prescribed for each of the colors to be displayed.

20

In one embodiment of the invention, the pixel defect detector further includes a determination section for determining a color to be displayed by the subject photoelectric transducer based on address data of the

- 9 -

subject photoelectric transducer. The reference photoelectric coefficient a_0 and the reference offset output level b_0 are prescribed based on the determination by the determination section.

5

Thus, the invention described herein makes possible the advantages of providing a pixel defect detector for a solid-state imaging device, which does not require a reference light generator, a special aided system, or the like, and with which the detection can be easily done by a user.

10

These and other advantages of the present invention will become apparent to those skilled in the art upon reading and understanding the following detailed description with reference to the accompanying figures.

15

BRIEF DESCRIPTION OF THE DRAWINGS

20

Figure 1 is a graph illustrating input/output characteristics of a photoelectric transducer;

Figure 2 is a plan view illustrating an arrangement of the primary colors in a color filter

- 10 -

according to a primary colors Bayer pattern;

Figure 3 is a block diagram illustrating a digital still camera incorporating a pixel defect detector according to one embodiment of the present invention;

Figure 4 is a flow chart illustrating a procedure performed by the apparatus shown in Figure 3;

Figure 5 is a block diagram illustrating a switching module provided in the apparatus shown in Figure 3; and

Figure 6 is a graph illustrating input/output characteristics of a photoelectric transducer.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

First, the general description of the present invention will be below.

As described above, the prior art procedure of applying reference light from a reference light generator across a solid-state imaging device, detecting a pixel

- 11 -

having a blemish based on an output signal of the solid-state imaging device using a special aided system, and then correcting the output of such a pixel, is far from that which can ordinarily be done by a user.

5

It is assumed in the present invention that no reference light generator for applying reference light is used. In such a case, the amount of light incident upon the solid-state imaging device cannot be determined, and a pixel having a blemish cannot be identified simply by comparing an output signal of a pixel with that of another neighboring pixel. For example, referring to the graph of Figure 1, when comparing an output characteristic $f_1(x)$ of a normal (i.e., not blemished) pixel in response to an amount of incident light with an output characteristic $f_2(x)$ of a pixel having a black blemish in response to an amount of incident light, the difference (e.g., Δy_1 or Δy_2) between the output characteristic $f_1(x)$ and the output characteristic $f_2(x)$ varies depending upon the amount of incident light. Therefore, a pixel having a blemish cannot be identified based on such a difference unless the amount of incident light can be accurately determined. When one attempts to determine the presence/absence of a blemish based on only one

- 12 -

difference between the output characteristic $f_1(x)$ and the output characteristic $f_2(x)$, the presence/absence of a blemish cannot be determined in a precise manner, as a white blemish and a black blemish cannot be accurately distinguished from each other.

Thus, according to the present invention, a pixel having a blemish is identified by using a function of Expression (1) below which represents the output characteristic of a pixel of the solid-state imaging device.

$$y(x)=ax+b \quad \dots (1)$$

Herein, $y(x)$ denotes an output level of a pixel (or a photoelectric transducer) being examined (hereinafter, a "subject pixel"), x denotes an amount of incident light, a denotes a photoelectric coefficient of the pixel, and b denotes an offset output level of the pixel in the absence of incident light.

When comparing the function of one normal pixel with that of another normal pixel, the values of the coefficients a and b for one pixel are close to those for

- 13 -

the other pixel, respectively. Moreover, the values of the coefficients a and b for each normal pixel are substantially equal to an average reference photoelectric coefficient a_0 and an average reference offset output level b_0 . For a pixel having a black blemish, the photoelectric coefficient a is smaller than the reference photoelectric coefficient a_0 . For a pixel having a white blemish, the offset output level b is larger than the reference offset output level b_0 .

10

The photoelectric coefficient a and the offset output level b of one pixel are constant regardless of the amount of light incident thereupon. Therefore, the presence/absence and the type of defect of the pixel can be determined by obtaining the above-described function for the pixel.

15

When N different amounts of incident light (from dark to bright), x_0, x_1, \dots, x_{N-1} , are applied to a subject pixel, thereby obtaining N output levels, y_0, y_1, \dots, y_{N-1} , corresponding to the subject pixel, the input/output relationship of the subject pixel can be represented by Expression (5), which is below based on Expression (1) above.

20

$$\begin{matrix} 5 \\ \\ \end{matrix}
 \begin{bmatrix} y_0 \\ y_1 \\ \vdots \\ y_{N-1} \end{bmatrix} = \begin{bmatrix} x_0 & 1 \\ x_1 & 1 \\ \vdots & \vdots \\ x_{N-1} & 1 \end{bmatrix} \begin{bmatrix} a \\ b \end{bmatrix} \quad \dots (5)$$

By using a least squares method, the photoelectric coefficient a and the offset output level b of a subject pixel can be obtained by applying the amount of light x_i incident upon the subject pixel and the actual output level y_i for the amount of light x to Expression (7) below, on the condition that the sum σ of squares of errors obtained by Expression (6) below is minimized (for the least squares method, see, for example, "Sensor and Signal Processing System II", pp. 10-11, 1985, Japan Society of Mechanical Engineers eds, published by Asakura Shoten).

$$\begin{matrix} 20 \end{matrix}
 \sigma = \sum_{i=0}^{N-1} (y(x_i) - y_i)^2 \quad \dots (6)$$

$$\begin{bmatrix} \sum x_i^2 & \sum x_i \\ \sum x_i & N \end{bmatrix} \begin{bmatrix} a \\ b \end{bmatrix} = \begin{bmatrix} \sum x_i y_i \\ \sum y_i \end{bmatrix} \quad \dots (7)$$

- 15 -

Since no reference light generator is used in the present invention, the amount of light x_i incident upon a subject pixel is not the amount of reference light, and thus is not readily specified. Therefore, the amount of
5 incident light x_i is estimated before obtaining the photoelectric coefficient a and the offset output level b from Expression (7) above.

In order to estimate the amount of light x_i
10 incident upon the subject pixel, the outputs of the subject pixel and the neighboring pixels within a prescribed pixel region are examined as follows. The term "pixel region" as used in this specification refers to a prescribed region on an imaging screen within which is included a number
15 of pixels, including the subject pixel. The output levels of some or all of the number of pixels are examined to estimate the amount of light incident upon the subject pixel. For example, the outputs of the subject pixel and other neighboring pixels, which are located along the
20 upper, lower, left and right sides of the subject pixel, are examined. If the number of pixels having the same type of defect is less than or equal to half of the number of the selected pixels, it is possible to obtain an output level of a normal pixel by obtaining the median y_0 of the

- 16 -

output levels of the selected pixels through a median filter represented by Expression (8) below.

$$\begin{aligned} \bar{y} &= \text{median}\{y_1, y_2, y_3, y_4, y_5\} \\ &= y_1; \text{ when } y_2 < y_3 < y_1 < y_4 < y_5 \dots (8) \end{aligned}$$

Herein, y_i ($i=1, \dots, 5$) denotes the output level of the five pixels selected from the same pixel region.

10

For example, where y_1 denotes the output level of a normal pixel, y_2 and y_3 denote the output levels of pixels having a black blemish (smaller than the output level of the normal pixel), and y_4 and y_5 denote the output levels of pixels having a white blemish (larger than the output level of the normal pixel), then, the median y_0 obtained through the median filter is equal to the output level y_1 of the normal pixel. Alternatively, when only normal pixels are selected from a pixel region, the median among the output levels of the normal pixels is obtained.

20

Once an output level y_0 of a normal pixel is obtained, it is possible to obtain the amount of light x incident upon the subject pixel by applying the output

- 17 -

level y_0 to Expression (2) below.

$$x = (y_0 - b_0) / a_0 \quad \dots (2)$$

5 Herein, a_0 denotes an average reference photoelectric coefficient among the normal pixels, and b_0 denotes an average reference offset output level among the normal pixels.

10 The output level y_0 of a normal pixel in the pixel region is obtained for each of N different amounts of light (from dark to bright), x_0, x_1, \dots, x_{N-1} , being applied. The output level y_0 is applied to Expression (2) above, so as to calculate each of the amounts of incident light,
15 x_0, x_1, \dots, x_{N-1} .

 Once the amounts of light, x_0, x_1, \dots, x_{N-1} , incident upon the subject pixel are determined as described above, it is possible to determine the
20 photoelectric coefficient a and the offset output level b of the subject pixel by applying, to Expression (7) above, the determined amounts of incident light and the actual output levels y_0, y_1, \dots, y_{N-1} , from the subject pixel in response to the amounts of incident light.

For displaying multiple colors, the reference photoelectric coefficient a_0 varies for the respective colors to be displayed. For example, in order to produce a color display with the three primary colors R, G and B, it may be necessary to prescribe a reference photoelectric coefficient a_0 for each of the primary colors R, G and B. In such a case, a subject pixel in a pixel region and pixels which are in the same pixel region and which represent the same color as that of the subject pixel are selected, and the median output level among the selected pixels is obtained by Expression (8) above. Then, the amount of incident light x is calculated based on Expression (2) above, and the photoelectric coefficient a is calculated based on Expression (7) above.

Instead of a_0 in Expression (2) above, values a_{0R} , a_{0G} and a_{0B} are used which denote predetermined reference photoelectric coefficients for the primary colors R, G and B, respectively. Moreover, the photoelectric coefficients obtained for the primary colors R, G and B are denoted by a_R , a_G and a_B , respectively.

It is not necessary to obtain the offset output

- 19 -

level b and the reference offset output level b_0 for each of the primary colors, but these values can be commonly used among the primary colors.

5 Where no special device, such as a reference light generator, is used as in the present invention, it is very difficult to make a uniform amount of light be incident across the solid-state imaging device. Therefore, in the present invention, it may be necessary to minimize the
10 size of a pixel region including the subject pixel so that a uniform amount of light is incident across the pixel region.

 Figure 2 illustrates a color filter of a primary
15 colors Bayer pattern. When such a color filter is employed for producing a color display, a pixel region **mask1** is prescribed surrounding a subject pixel $R1$ for the primary color R , and nine pixels R_1, R_0 are selected from the pixel region **mask1** to obtain the amount of light
20 incident upon the subject pixel. Similarly, for the primary color B , a pixel region **mask2** is prescribed surrounding a subject pixel $B1$ and nine pixels B_1, B_0 are selected from the pixel region **mask2** to obtain the amount of light incident upon the subject pixel. For the primary

- 20 -

color G, a pixel region **mask3** or **mask4** is prescribed surrounding a subject pixel **G1** and nine or five pixels **G₁**, **G₀** are selected from the pixel region **mask3** or **mask4** to obtain the amount of light incident upon the subject pixel.

5

The relatively small pixel region **mask4** is used to obtain an accurate amount of light incident upon the subject pixel **G1** because the human eye is most sensitive to the primary color G of the three primary colors.

10

In this way, the amount of incident light and the output level are obtained for each subject pixel, so as to obtain one of the photoelectric coefficients a_R , a_G and a_B corresponding to the color displayed by the subject pixel, and the reference offset output level b_0 which is common to the primary colors R, G and B. Then, the presence/absence and the type of defect are determined based on Expressions (9), (10) and (11) below.

15

20

Where the subject pixel displays the primary color R, the subject pixel has:

no defect if $|a_{0R} - a_R| < \Delta a_R$ and $|b_0 - b| < \Delta b_R$;

a black blemish if $|a_{0R} - a_R| \geq \Delta a_R$; and

a white blemish if $|b_0 - b| \geq \Delta b_R$.

- 22 -

be non-zero, i.e., Expression (12) below needs to hold. In other words, the series of N functions in Expression (5) above need to be independent from one another. Under a condition close to the ideal condition (e.g., where

5 substantially no white noise exists), the series of N functions are correlated to one another, whereby Expression (12) below does not hold. Then, the photoelectric coefficient $a=a_0$, and the offset output level $b=b_0$.

10

$$\left| \begin{array}{cc} \sum x_i^2 & \sum x_i \\ \sum x_i & N \end{array} \right| \neq 0 \quad \dots (12)$$

15

In the manner as described above, the presence/absence and the type of defect are determined for each subject pixel. Then, if a subject pixel has a defect, the output level of the pixel will be corrected.

20

The bias voltage added to the output of a pixel having a white blemish varies depending upon the temperature, while the sensitivity of a pixel having a black blemish is deteriorated. Therefore, simple addition or subtraction of a constant voltage to/from the

- 23 -

output of such a pixel may not sufficiently correct the output of the pixel. It is preferred to correct the output signal of a pixel having a defect using the output signal of each of one or more neighboring pixels.

5

For correcting the output signal of the pixel having a defect, using the output signal of each of one or more pixels which are horizontally neighboring the subject pixel along with those of one or more pixels which are vertically neighboring the subject pixel is more preferred for the following reason than using only the output signal of one or more pixels which are neighboring the subject pixel either in the horizontal or vertical direction. For example, when the subject pixel having a defect is located along an edge of the object being imaged, the output of a pixel horizontally neighboring the subject pixel is likely to be substantially different from that of a pixel vertically neighboring the subject pixel. In such a case, correcting the output signal of the subject pixel using only the output signal of a pixel neighboring the subject pixel either in the horizontal or vertical direction may result in false colors.

Moreover, it is preferred to perform the output

- 24 -

level correction separately for the respective primary colors to be displayed. For example, where the primary colors R, G and B are displayed, it is preferred to perform a more precise correction for the primary color G using the color distribution of the primary colors Bayer pattern because the primary color G is more important than the other primary colors R and B.

For example, as illustrated in Figure 2, the pixel region **mask1** is prescribed surrounding the subject pixel **R1** for the primary color R, while the pixel region **mask2** is prescribed surrounding the subject pixel **B1** for the primary color B. Assuming that the corrected output level of the subject pixel **R1** or **B1** is $y(i,j)$, the average of the output levels of the other eight pixels, which are in the same pixel region and which represent the same color as that of the subject pixel **R1** or **B1**, is determined based on Expression (13) below.

$$\begin{aligned}
 y(i,j) = & [y(i-2,j-2)+y(i,j-2)+y(i+2,j-2) \\
 & +y(i-2,j)+y(i+2,j)+y(i-2,j+2) \\
 & +y(i,j+2)+y(i+2,j+2)]/8 \quad \dots (13)
 \end{aligned}$$

For the primary color G, the pixel region **mask4**

- 25 -

is prescribed surrounding the subject pixel G1, also as illustrated in Figure 2. Assuming that the corrected output level of the subject pixel G1 is $y(i,j)$, the average of the output levels of the other four pixels, which are

5 in the same pixel region and which represent the same color as that of the subject pixel G1, is determined based on Expression (14) below.

$$\begin{aligned}
 y(i,j) = & [y(i-1,j-1) + y(i+1,j-1) + y(i-1,j+1) \\
 10 \quad & + y(i+1,j+1)] / 4 \quad \dots (14)
 \end{aligned}$$

In Expressions (13) and (14), designations such as "(i,j)" denote coordinate points.

15 The relatively small pixel region mask4 is used to obtain an accurate amount of light incident upon the subject pixel G1 because the human eye is most sensitive to the primary color G of the three primary colors.

20 A pixel defect detector according to one embodiment of the present invention is used for detecting a pixel defect in a solid-state imaging device (e.g., a CCD) such as a digital still camera. The digital still camera is provided with a special mode of operation for

- 26 -

the pixel defect detection. In the special mode, the detection of a defect in a solid-state imaging device is substantially automatically performed in response to a predetermined operation by a user. Thus, the position of a pixel having a defect and the output characteristics of the pixel are recorded. Then, in a normal shooting mode, output signals from each pixel having a defect are corrected based on the recorded data.

10 For the pixel defect detection, a γ correction is not performed on the output signal of the solid-state imaging device so that the amount of error occurring in the output signal from the pixel having a defect will not be reduced.

15 In the pixel defect detector according to this embodiment of the present invention, the output characteristics of the solid-state imaging device are determined by applying varied amounts of light to the solid-state imaging device. Various functions of the digital still camera such as the diaphragm, the strobe, and the shutter speed, may be used alone or in combination so as to vary the amounts of light incident upon the solid-state imaging device.

20

- 27 -

In the pixel defect detection mode, the digital still camera is preferably defocused so that a uniform level of light is incident across the solid-state imaging device. For example, even when a wall or a panel having a uniform gray level is imaged by the digital still camera so as to obtain a uniform level of light incident across the solid-state imaging device, there may be some non-uniformity in the gray level of the object and/or some non-uniformity in the illumination for the object. By defocusing the digital still camera, it is possible to blur such non-uniformity, thereby applying a more uniform level of light incident across the solid-state imaging device.

Figure 3 is a block diagram illustrating a digital still camera incorporating the pixel defect detector according to one embodiment of the present invention. Referring to Figure 3, light is incident upon a solid-state imaging device (e.g., a CCD) 4 via a lens section 1, a diaphragm 2 and a shutter 3, thereby projecting a picture onto an imaging screen of the solid-state imaging device 4. The solid-state imaging device 4 includes a plurality of photoelectric

- 28 -

transducers (pixels) arranged in rows and columns, and the picture is projected onto such an arrangement of pixels. The output signals from these pixels are successively sent to a picture processing section 6 via a switching module 5.

5 A strobe 7 emits light in synchronism with the opening/closing of the shutter 3 so as to irradiate the object. A group of operation keys 8 are used by a user to operate the digital still camera.

10 The picture processing section 6 primarily comprises a picture memory 11, an EEPROM 12, a data table 13, a control signal producing section 14, and a processor 15.

The picture memory 11 stores the output levels of the pixels of the solid-state imaging device 4, i.e., picture data representing the imaged picture. Typically, the picture memory 11 is capable of storing at least three pictures. The EEPROM 12 stores: photoelectric coefficients a_{0R} , a_{0G} , a_{0B} , threshold values Δa_R , Δa_G , Δa_B , and Δb_R , Δb_G , Δb_B , which are predetermined for the respective primary colors R, G and B; the reference offset output level b_0 ; and the size of the imaging screen of the solid-state imaging device 4 (represented by the number of pixels along the horizontal direction I_0 times that along the vertical direction J_0 on the imaging screen).

15

20

- 29 -

The data table 13 stores various data such as data used for performing a γ correction on the picture data, and data used for performing a JPEG compression on the picture data. The control signal producing section 14 produces and
5 outputs a control signal for controlling, in response to an instruction from the processor 15, the lens section 1, the diaphragm 2, the shutter 3, the solid-state imaging device 4, the switching module 5, the strobe 7, etc. The processor 15 controls the picture processing section 6,
10 processes picture data and performs various other arithmetic operations.

The picture processing section 6 may be produced on a single-chip LSI.
15

In the digital still camera of this embodiment of the present invention, the normal shooting mode can be selected by operating the group of operation keys 8.

20 In the normal shooting mode, in response to an appropriate operation of the group of operation keys 8, the processor 15: drives the lens section 1 so as to focus the picture being projected onto the imaging screen of the solid-state imaging device 4 (auto-focus); adjusts

- 30 -

the stop of the diaphragm 2; opens/closes the shutter 3; and controls the strobe 7 to emit light in synchronism with the opening/closing of the shutter 3. As a result, the picture is imaged by the solid-state imaging device 4.

5 Then, the processor 15: receives the picture data from the solid-state imaging device 4 via the switching module 5; temporarily stores the picture data in the picture memory 11; performs image processes (e.g., a γ correction and an image compression) on the picture data;
10 and sends the processed picture data to a recording mechanism of a recording medium (not shown). The recording mechanism records the processed picture data on the recording medium.

15 In the digital still camera of this embodiment of the present invention, the pixel defect detection mode can be selected by operating the group of operation keys 8. When the pixel defect detection mode is selected, the processor 15 performs the procedure illustrated in the
20 flow chart of Figure 4 so as to automatically detect a pixel having a defect and store the coordinate position of the pixel in the EEPROM 12. Thus, the detection of a defect pixel can be done by a user without requiring any special device or knowledge.

- 31 -

First, the processor 15 prepares an area in the picture memory 11 for storing three pictures, and inactivates functions such as the γ correction, the image compression, and the auto-focus of the lens section 1 (step 101). The processor 15 further drives the lens section 1 so as to set the focus of the lens section 1 to ∞ , for example (step 102).

After setting the release time of the shutter 3 to zero, the switching module 5 receives the output signal from each pixel of the solid-state imaging device 4 via the switching module 5, and stores the output levels of the pixels in a first picture data storage area of the picture memory 11 (step 103).

When the release time of the shutter 3 is set to zero, the amount of light incident upon each pixel of the solid-state imaging device 4 is zero, whereby the output of each pixel is at the lowest level.

Subsequently, the processor 15 sets the diaphragm 2 to "open", and opens/closes the shutter 3, while controlling the strobe 7 to emit light in synchronism with

- 32 -

the opening/closing of the shutter 3. Then, the processor 15 receives the output signal from each pixel of the solid-state imaging device 4 via the switching module 5, and stores the output levels of the pixels in a second picture data storage area of the picture memory 11 (step 104). -

In the above-described operation, the release time of the shutter 3 is set so that an amount of light is incident upon each pixel, which brings the solid-state imaging device 4 to a near-overflow state (i.e., the amount of incident light is maximized without bringing the solid-state imaging device 4 to the overflow state), thereby maximizing the output level of the pixel. As described above, it is preferable for detecting a pixel having a defect to use a wall or a panel having a uniform gray level, so that the picture imaged by the solid-state imaging device 4 has a substantially uniform gray scale. Moreover, since the focus of the lens section 1 is adjusted to ∞ , the picture imaged by the solid-state imaging device 4 is blurred to provide an even more uniform gray scale. Such a picture having a uniform gray scale is preferred in detecting a pixel having a particularly low output level (i.e., a particularly dark pixel) or a pixel

- 33 -

having a particularly high output level (i.e., a particularly bright pixel), i.e., a pixel having a defect.

Moreover, the processor 15 sufficiently stops
5 down the diaphragm 2, and opens/closes the shutter 3,
while controlling the strobe 7 to emit light in
synchronism with the opening/closing of the shutter 3.
Then, the processor 15 receives the output signal from
each pixel of the solid-state imaging device 4 via the
10 switching module 5, and stores the output levels of the
pixels in a third picture data storage area of the picture
memory 11 (step 105).

Since the shutter 3 is opened/closed while
15 sufficiently stopping down the diaphragm 2 with the
strobe 7 emitting light, the amount of light incident upon
each pixel of the solid-state imaging device 4, and thus
the output level of each pixel, are between those of
step 103 and those of step 104, respectively. The
20 photographed object of the digital still camera is
preferably the same in steps 104 and 105. In step 105,
it is preferable to image a wall or a panel having a uniform
gray level, as described above. Since the focus of the
lens section 1 is adjusted to ∞ , the picture imaged by

- 34 -

the solid-state imaging device 4 is blurred to provide a uniform gray scale.

In this way, the following three output levels are
5 stored in the picture memory 11 in steps 103, 104 and 105,
respectively: the output level of each pixel of the
solid-state imaging device 4 for the minimum amount of
incident light; the output level of each pixel of the
solid-state imaging device 4 for an intermediate amount
10 of incident light; and the output level of each pixel of
the solid-state imaging device 4 for the maximum amount
of incident light.

Thus, the input/output relationship as repre-
15 sented by Expression (5) above ($N-1=2$) holds for each
pixel of the solid-state imaging device 4.

Then, the processor 15 successively addresses the
pixels of the solid-state imaging device 4 by successively
20 designating addresses, i.e., the coordinate positions
(i, j). Each time a coordinate position (i, j) is desig-
nated, it is determined which of the primary colors R,
G and B is to be displayed by the pixel at the designated
coordinate position (i, j) based on the coordinates i and

- 35 -

j of the position, while calculating the above-described function for the addressed pixel so as to determine the presence/absence and the type of defect of the pixel.

5 In this embodiment of the present invention, a color filter of a primary colors Bayer pattern as illustrated in Figure 2 is used, whereby it is possible to determine which of the primary colors R, G and B is to be displayed by the pixel at the coordinate position (i,j) based on the coordinates i and j of the position.

10

 First, the processor 15 initializes the coordinate position (i,j) to (0,0). Moreover, the processor 15 reads out, from the EEPROM 12, various data such as: the photoelectric coefficients a_{0R} , a_{0G} , a_{0B} , threshold values Δa_R , Δa_G , Δa_B , and Δb_R , Δb_G , Δb_B , which are predetermined for the respective primary colors R, G and B; the reference offset output level b_0 ; and the size of the imaging screen of the solid-state imaging device 4 (represented by the number of pixels along the horizontal direction (I0-1) times that along the vertical direction (J0-1) on the imaging screen) (step 106).

15

20

- 36 -

Then, the processor 15 determines whether each of the coordinates i and j of the coordinate position (i, j) is an even number (steps 107, 108).

5 After the initialization, each of the initialized coordinates $i=0$ and $j=0$ is determined to be an even number (i.e., "yes" is given at step 107 and at step 108). In the primary colors Bayer pattern as illustrated in Figure 2, any pixel at a coordinate position where the
10 coordinates i and j are both an even number displays the primary color R. Therefore, the presence/absence and the type of defect of the pixel displaying the primary color R are determined (step 109).

15 In step 109, the processor 15 assumes the pixel at the coordinate position $(0, 0)$ to be the subject pixel, and extracts the output levels of all the necessary pixels from the three pictures of data, which have been stored in the picture memory 11 in steps 103-105, respectively.
20 The processor 15 further determines the amount of light incident upon the subject pixel based on Expression (8) and Expression (2) above, determines the photoelectric coefficient a and the offset output level b of the subject pixel based on Expression (7) above, and determines the

- 37 -

presence/absence and the type of defect of the subject pixel based on Expression (9) above. If the subject pixel has a defect, the type of defect (i.e., a white blemish or a black blemish) and the coordinate position of the subject pixel are stored in the picture memory 11.

Thereafter, the processor 15 determines whether the next coordinate position (i+1,j) to the right falls beyond the horizontal size (I0-1) of the imaging screen (step 110). If "No" is given at step 110, the processor 15 then adds 1 to i so as to update the coordinate position (i,j) (step 111). Then, the process returns to step 107.

The coordinate i of the updated coordinate position (i,j) is now 1, an odd number. Thus, i is determined to be an odd number ("No" is given at step 107), while j is still determined to be an even number ("Yes" is given at step 115). In the primary colors Bayer pattern as illustrated in Figure 2, any pixel at such a coordinate position displays the primary color G. Therefore, the presence/absence and the type of defect of the pixel displaying the primary color G are determined (step 112).

- 38 -

In step 112, the processor 15 assumes the pixel at the coordinate position (1,0) to be the subject pixel, and extracts the output levels of all the necessary pixels from the three pictures of data, which have been stored in the picture memory 11 in steps 103-105, respectively. The processor 15 further determines the amount of light incident upon the subject pixel based on Expression (8) and Expression (2) above, determines the photoelectric coefficient *a* and the offset output level *b* of the subject pixel based on Expression (7) above, and determines the presence/absence and the type of defect of the subject pixel based on Expression (10) above. If the subject pixel has a defect, the type of defect (i.e., a white blemish or a black blemish) and the coordinate position of the subject pixel are stored in the picture memory 11.

In the primary colors Bayer pattern as illustrated in Figure 2, the pixels at coordinate positions (i,0) alternately correspond to the primary colors R and G. Therefore, subsequently for other pixels at coordinate positions (i,0), step 109 and step 112 are alternately performed as described above so as to determine the presence/absence and the type of defect of the subject

- 39 -

pixel at each coordinate position (i,0) while storing such data for any subject pixel having a defect.

When i+1 is equal to I0 ("Yes" is given at step 110),
5 the processor 15 then determines whether the next coordinate position (i,j+1) below the position (i,j) falls beyond the vertical size (J0-1) of the imaging screen (step 113). If "No" is given at step 113, the processor 15 then adds 1 to j so as to update the coordinate
10 j to 1 (step 114). Since the coordinate j is an odd number ("No" is given at step 108 and at step 115), either step 112 or step 116 is performed depending upon the determination of whether the coordinate i is an even number (step 107).

15

In step 112, the presence/absence and the type of defect of the pixel displaying the primary color G are determined as described above. In step 116, the presence/absence and the type of defect of the pixel displaying
20 the primary color B are determined. For the determination, the processor 15 extracts the output levels of all the necessary pixels from the three pictures of data, which have been stored in the picture memory 11 in steps 103-105, respectively. The processor 15 further determines the

- 40 -

amount of light incident upon the subject pixel based on Expression (8) and Expression (2) above, determines the photoelectric coefficient a and the offset output level b of the subject pixel based on Expression (7) above, and
5 determines the presence/absence and the type of defect of the subject pixel based on Expression (11) above. If the subject pixel has a defect, the type of defect (i.e., a white blemish or a black blemish) and the coordinate position of the subject pixel are stored in the picture
10 memory 11.

In the primary colors Bayer pattern as illustrated in Figure 2, the pixels at coordinate positions (i,1) alternately correspond to the primary colors B and G.
15 Therefore, subsequently for other pixels at coordinate positions (i,1), step 116 and step 112 are alternately performed as described above so as to determine the presence/absence and the type of defect of the subject pixel at each coordinate position (i,1) while storing such
20 data for any subject pixel having a defect.

Subsequently, as the coordinate j is incremented, the coordinate i is successively varied from 0 to (I0-1) so as to determine the presence/absence and the type of

- 41 -

defect for each pixel at a coordinate position (0-
(I0-1),j). The determination of the presence/absence and
the type of defect for all the pixels is complete when
i+1 is determined to be equal to (I0-1) ("Yes" is given
5 at step 110) while j+1 is determined to be equal to (J0-1)
("Yes" is given at step 113). Then, the processor 15
reads out the type of defect of each pixel having a defect
and the coordinate position thereof from the picture
memory 11, and stores the data in the EEPROM 12.

10

When the above-described shooting mode is se-
lected after the type of defect of each pixel of the
solid-state imaging device 4 having a defect and the
coordinate position thereof have been stored in the
15 EEPROM 12, the following process is performed to correct
the output level of each defect pixel.

As the normal shooting mode is selected by the
operation of the group of operation keys 8, a picture is
20 imaged by the solid-state imaging device 4, and the
processor 15 receives picture data from the solid-state
imaging device 4 via the switching module 5.

The switching module 5 has a structure as

- 42 -

illustrated in Figure 5, and comprises three terminals SW1, SW2 and SW3. When a segment 5a is switched to the terminal SW1, the output of the solid-state imaging device 4 is directly transmitted to the processor 15, thereby storing the output level of each pixel in the picture memory 11.

The processor 15 reads out the coordinate position (i,j) of a pixel having a defect from the EEPROM 12, so as to identify the color displayed by the pixel having a defect based on the coordinates i and j, and switches the switching module 5 to the terminal SW2 or SW3 at a timing at which a signal is output from the pixel. In particular, the processor 15 determines the color displayed by the pixel having a defect based on the coordinates i and j in a manner similar to that of steps 107, 108 and 115. Then, if the color displayed by the pixel is determined to be R or B, the processor 15 switches the switching module 5 to the terminal SW2 at a timing at which a signal is output from the pixel. If the color displayed by the pixel is determined to be G, the processor 15 switches the switching module 5 to the terminal SW3 at a timing at which a signal is output from the pixel.

- 43 -

When the processor 15 receives the output signal of a defect pixel of the primary color R or B through the terminal SW2 of the switching module 5, the processor 15 performs the calculation of Expression (13) above so as to determine the output level of the defect pixel (after data necessary for performing the calculation of Expression (13) above has been transmitted from the solid-state imaging device 4 to the picture memory 11), and stores the determined output level in an address in the picture memory 11 for the defect pixel.

When the processor 15 receives the output signal of a defect pixel of the primary color G through the terminal SW3 of the switching module 5, the processor 15 performs the calculation of Expression (14) above so as to determine the output level of the defect pixel (after data necessary for performing the calculation of Expression (14) above has been transmitted from the solid-state imaging device 4 to the picture memory 11), and stores the determined output level in an address in the picture memory 11 for the defect pixel.

Thus, by switching the segment 5a of the switching

- 44 -

module 5 to the terminal SW1, SW2 or SW3, it is possible to correct the output level of the defect pixel in a substantially real-time manner while storing the picture data from the solid-state imaging device 4 in the picture memory 11.

Thereafter, image processes (e.g., a γ correction and an image compression) are performed on the picture data stored in the picture memory 11, and the processed picture data is sent to a recording mechanism of a recording medium (not shown) so as to record the processed picture data on the recording medium.

Instead of determining the color displayed by the pixel having a defect based on the coordinates i and j in a manner similar to that of steps 107, 108 and 115 as illustrated in Figure 4, it is possible to perform the determination while utilizing the fact that i or j is an even number when the least significant bit thereof is "0", and is an odd number when it is "1". Thus, the color displayed by the pixel is determined to be R or B if the XOR between i and j is false, and G if it is true.

While the above-described pixel defect detection

- 45 -

mode is selected, the segment 5a is always connected to the terminal SW1.

As is apparent from the description above, with
5 the digital still camera of this embodiment of the present invention, it is possible to automatically detect the coordinate position of a pixel having a defect and the type of defect and correct the output signal of the defect pixel, only by obtaining three pictures of data through
10 multiple picture shooting operations, without requiring a reference light generator, a special aided system, or the like. Therefore, even a user can easily perform the detection and correction of defect pixels.

15 The present invention is not limited to the above-described particular embodiment. Various modifications can be made to the above-described embodiment. For example, while the photoelectric coefficient a and the offset output level b of each pixel are determined
20 based on three output levels therefrom in response to three different amounts of incident light, respectively, in the above-described embodiment, the photoelectric coefficient a and the offset output level b may alternatively be determined based on two or four output levels for two

or four different amounts of incident light.

When the photoelectric coefficient a and the offset output level b are determined based on two output levels for two different amounts of incident light, Expressions (5), (6) and (7) above may be unnecessary. Instead, the photoelectric coefficient a and the offset output level b may be obtained by determining two different amounts of incident light based on Expressions (8) and (2) and detecting two output levels from a pixel in response to the two amounts of incident light.

In the above-described embodiment, an output of a pixel is approximated by a linear function so that the scale of the arithmetic circuit, the amount of calculation and the time required for calculation are within the practical range. Strictly speaking, the output characteristic of a photoelectric transducer is non-linear. When it is difficult to approximate the output characteristic of the photoelectric transducer with a single linear function, a combination of a plurality of linear functions or another type of function may be used provided that the possible increase in the scale of the arithmetic circuit, the amount of calculation and the time

- 47 -

required for calculation is minimized. With any type of function being used, the presence/absence and the type of defect of a pixel can be determined by representing the actual output characteristic of the pixel in the function and comparing the coefficients of the function with one another.

The output level of a normal pixel may alternatively be determined by any appropriate means other than the use of the median filter of Expression (8) above. For example, the output level of a normal pixel may be determined by using the median filter after excluding the maximum and minimum output levels among those of the pixels neighboring the subject pixel within the same pixel region, or alternatively by performing a well-known statistical operation on the output levels of the pixels neighboring the subject pixel within the same pixel region. Moreover, various pixel regions may be prescribed surrounding the subject pixel, other than those described above.

20

While a CCD is described as an exemplary solid-state imaging device in the above-described embodiment, the present invention is not limited thereto. For example, the present invention may alternatively be

- 48 -

used for other types of solid-state imaging device such as CID or CPD. Moreover, the present invention may alternatively be used for solid-state imaging apparatuses other than a digital still camera, such as a video camera, or a film scanner.

While a color filter of a primary colors Bayer pattern is described in the above-described embodiment, the present invention may alternatively be used with other types of color filter in which primary colors and complementary colors are arranged according to a given rule.

As described above, according to the present invention, output characteristics of a subject photoelectric transducer in response to varied amounts of light incident upon the subject photoelectric transducer are determined, so as to detect a defect in the subject photoelectric transducer based on the output characteristics.

Alternatively, according to the present invention, outputs of a subject photoelectric transducer in response to different amounts of incident light may be stored in

- 49 -

the picture memory so as to determine the photoelectric coefficient a of the subject photoelectric transducer and the offset output level b of the subject photoelectric transducer in the absence of incident light, based on the amounts of incident light, the outputs of the subject photoelectric transducer therefor, and Expression (1). A defect in the subject photoelectric transducer is detected by comparing the photoelectric coefficient a and the offset output level b with a predetermined reference photoelectric coefficient a_0 and a predetermined reference offset output level b_0 , respectively.

$$y(x)=ax+b \quad \dots (1)$$

Herein, $y(x)$ denotes the output of the subject photoelectric transducer, and x denotes the amount of incident light.

As described above, the output characteristic of the subject photoelectric transducer is determined, so as to detect a defect of the subject photoelectric transducer based on the output characteristic. In this way, a reference light generator, a special aided system, or the like, as in the prior art, is not required, whereby

- 50 -

even an ordinary user can easily detect a defect of a photoelectric transducer. Not only the presence/absence of the defect, but also the type of defect, can be determined.

5

Moreover, according to the present invention, the output of a photoelectric transducer is determined while the solid-state imaging device is defocused. Therefore, a substantially uniform light is incident upon a pixel region surrounding the subject photoelectric transducer. Therefore, it is possible to identify the output signal of a normal photoelectric transducer based on output signals from a plurality of photoelectric transducers in the same pixel region, thereby estimating the actual amount of incident light.

10
15

Furthermore, an amount of incident light which is measured when no light is incident upon the solid-state imaging device, and another amount of incident light which brings the solid-state imaging device to a near-overflow state. These amounts of incident light can easily be obtained by appropriately controlling the shutter speed, the diaphragm, the strobe, and the like, of a video camera or a digital still camera.

20

- 51 -

For a color display, photoelectric transducers for different colors are separately examined. Therefore, it is possible to accurately determine the presence/absence and the type of defect of the photoelectric
5 transducer. -

Moreover, for a color display, the color displayed by each pixel is determined based on address data (i.e.,
10 the coordinate position) of the pixel, thereby allowing the calculation to be performed faster.

Various other modifications will be apparent to and can be readily made by those skilled in the art without
15 departing from the scope and spirit of this invention. Accordingly, it is not intended that the scope of the claims appended hereto be limited to the description as set forth herein, but rather that the claims be broadly construed.

WHAT IS CLAIMED IS:

1. A pixel defect detector for a solid-state imaging device comprising a plurality of photoelectric transducers, the pixel defect detector comprising:

a calculation section for obtaining output characteristics of a subject photoelectric transducer for varied amounts of light incident thereupon so as to determine the presence/absence of a defect in the subject photoelectric transducer based on the output characteristics thereof.

2. A pixel defect detector for a solid-state imaging device according to claim 1, wherein:

the pixel defect detector further comprises a picture memory for storing an output signal from the photoelectric transducer; and

the calculation section determines the output characteristics of the subject photoelectric transducer using the output signal of the subject photoelectric transducer stored in the picture memory.

3. A pixel defect detector for a solid-state imaging device according to claim 1, wherein:

- 53 -

the output characteristics of the subject photoelectric transducer are represented by a plurality of output signals of the subject photoelectric transducer in response to different amounts of light incident thereupon, respectively.

4. A pixel defect detector for a solid-state imaging device comprising a plurality of photoelectric transducers, the pixel defect detector comprising:

a picture memory for storing outputs from a subject photoelectric transducer in response to different amounts of light incident thereupon, respectively; and

a calculation section for determining a photoelectric coefficient a of the subject photoelectric transducer and an offset output level b of the subject photoelectric transducer in the absence of incident light based on the amounts of incident light, the outputs from the subject photoelectric transducer therefor, and Expression (1) below, so as to compare the photoelectric coefficient a and the offset output level b with a predetermined reference photoelectric coefficient a_0 and a predetermined reference offset output level b_0 , respectively, thereby determining the presence/absence of a defect in the subject photoelectric transducer:

- 54 -

$$y(x)=ax+b \qquad \dots (1)$$

where $y(x)$ denotes the output of the subject photoelectric transducer, and x denotes the amount of incident light.

5. A pixel defect detector for a solid-state imaging device according to claim 1, wherein:

the pixel defect detector further comprises an optical system for projecting a picture onto the solid-state imaging device; and

the output of the photoelectric transducer is determined while the optical system is defocused with respect to the solid-state imaging device.

6. A pixel defect detector for a solid-state imaging device according to claim 4, wherein:

the pixel defect detector further comprises an optical system for projecting a picture onto the solid-state imaging device; and

the output of the photoelectric transducer is determined while the optical system is defocused with respect to the solid-state imaging device.

7. A pixel defect detector for a solid-state imaging

- 55 -

device according to claim 1, wherein:

the amounts of light incident upon the subject photoelectric transducer comprise an amount of incident light when no light is incident upon the solid-state imaging device and another amount of incident light which brings the solid-state imaging device to a near-overflow state.

8. A pixel defect detector for a solid-state imaging device according to claim 4, wherein:

the amounts of light incident upon the subject photoelectric transducer comprise an amount of incident light when no light is incident upon the solid-state imaging device and another amount of incident light which brings the solid-state imaging device to a near-overflow state.

9. A pixel defect detector for a solid-state imaging device according to claim 4, wherein:

the amount of incident light x is determined by applying, to Expression (2) below, the predetermined reference photoelectric coefficient a_0 , the predetermined reference offset output level b_0 , and a reference output signal y_0 :

- 56 -

$$x = (y_0 - b_0) / a_0 \quad \dots (2).$$

10. A pixel defect detector for a solid-state imaging device according to claim 9, wherein:

the output y_0 is set to a median among outputs from a plurality of photoelectric transducers neighboring the subject photoelectric transducer.

11. A pixel defect detector for a solid-state imaging device according to claim 10, wherein:

the neighboring photoelectric transducers comprise only those which display the same one of a plurality of colors to be displayed as that of the subject photoelectric transducer.

12. A pixel defect detector for a solid-state imaging device according to claim 4, wherein:

the presence/absence of a defect in the subject photoelectric transducer is determined by applying the photoelectric coefficient a of the subject photoelectric transducer, the offset output level b of the subject photoelectric transducer, the reference photoelectric coefficient a_0 , and the reference offset output level b_0 , to Expression (3) below:

- 57 -

no defect, if $|a_0 - a| < \Delta a$ and $|b_0 - b| < \Delta b$... (3)

where Δa and Δb are predetermined threshold values.

13. A pixel defect detector for a solid-state imaging device according to claim 4, wherein:

the presence/absence and the type of defect in the subject photoelectric transducer are determined by applying the photoelectric coefficient a of the subject photoelectric transducer, the offset output level b of the subject photoelectric transducer, the reference photoelectric coefficient a_0 , and the reference offset output level b_0 , to Expression (4) below:

no defect, if $|a_0 - a| < \Delta a$ and $|b_0 - b| < \Delta b$;

a black blemish, if $|a_0 - a| \geq \Delta a$; and

a white blemish, if $|b_0 - b| \geq \Delta b$...

(4)

where Δa and Δb are predetermined threshold values.

14. A pixel defect detector for a solid-state imaging device according to claim 12, wherein:

the reference photoelectric coefficient a_0 and the reference offset output level b_0 are prescribed for

- 58 -

each of the colors to be displayed.

15. A pixel defect detector for a solid-state imaging device according to claim 13, wherein:

the reference photoelectric coefficient a_0 and the reference offset output level b_0 are prescribed for each of the colors to be displayed.

16. A pixel defect detector for a solid-state imaging device according to claim 12, wherein:

the pixel defect detector further comprises a determination section for determining a color to be displayed by the subject photoelectric transducer based on address data of the subject photoelectric transducer; and

the reference photoelectric coefficient a_0 and the reference offset output level b_0 are prescribed based on the determination by the determination section.

17. A pixel defect detector for a solid-state imaging device according to claim 13, wherein:

the pixel defect detector further comprises a determination section for determining a color to be displayed by the subject photoelectric transducer based

- 59 -

on address data of the subject photoelectric transducer;
and

the reference photoelectric coefficient a_0 and the reference offset output level b_0 are prescribed based on the determination by the determination section.

- 60 -

ABSTRACT OF THE DISCLOSURE

A pixel defect detector of the present invention is used for a solid-state imaging device comprising a plurality of photoelectric transducers. The pixel defect detector includes: a calculation section for obtaining output characteristics of a subject photoelectric transducer for varied amounts of light incident thereupon so as to determine the presence/absence of a defect in the subject photoelectric transducer based on the output characteristics thereof.

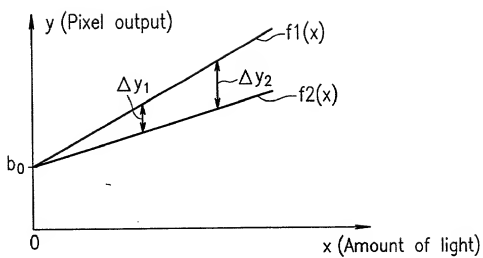
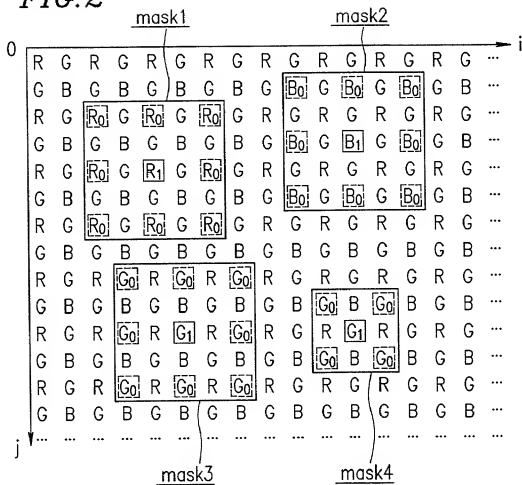
FIG. 1

FIG. 2



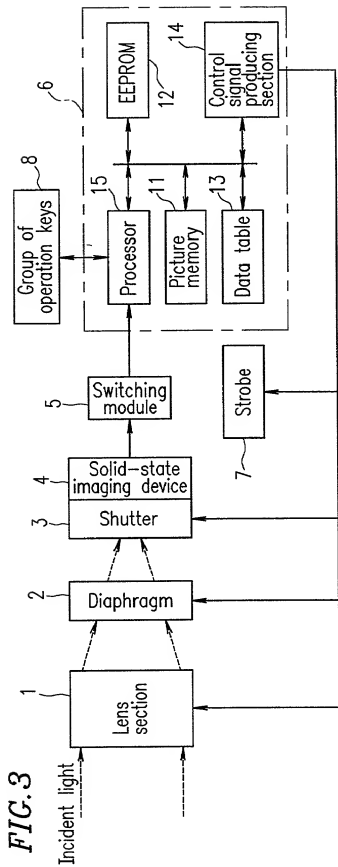


FIG. 4

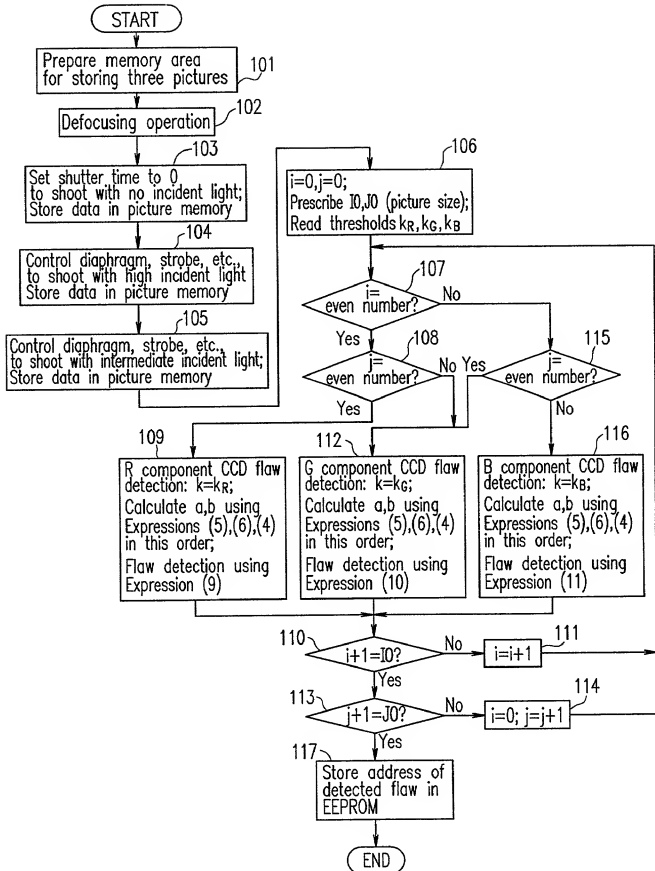
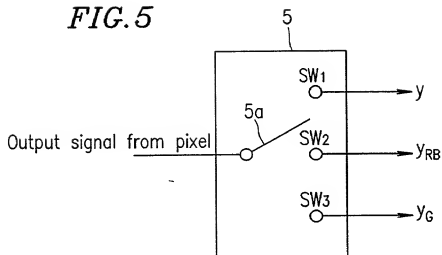


FIG. 5**FIG. 6**